



2182
JFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Manjunath Haritsa, Ralf M. Schmitt
Assignee: Sun Microsystems, Inc.
Title: SIMULATION BY PARTS METHOD FOR GRID-BASED CLOCK
DISTRIBUTION DESIGN
Serial No.: 09/982,458 Filing Date: October 17, 2001
Examiner: Unassigned Group Art Unit: 2182
Docket No.: P-5404

Monterey, CA
March 8, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRY

Dear Sir:

Applicant(s) have not received a first official action in the above application that was filed on October 17, 2001. What is the status of the above application and what is the expected date for issuance of a first official action?

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 8, 2005.

Respectfully submitted,

Philip McKay
Attorney for Applicant(s)
Reg. No. 38,966
(831) 655-0880

Attorney for Applicant(s)

March 8, 2005

Date of Signature